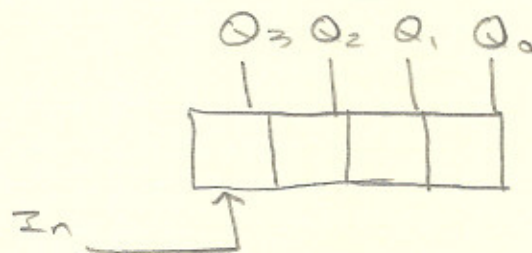


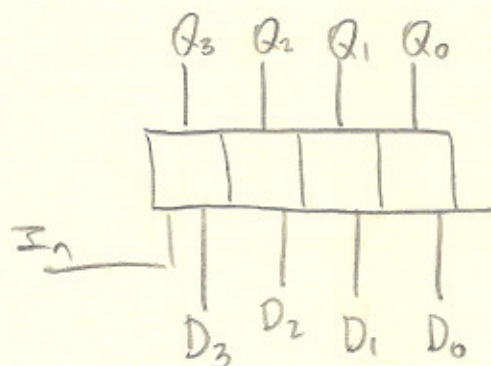
Counters

t	0	0	0	0
$t+1$	0	0	0	1
$t+2$	0	0	1	0
$t+3$	0	0	1	1

UP Counter

1	1	1	0
1	1	0	1
1	1	0	0

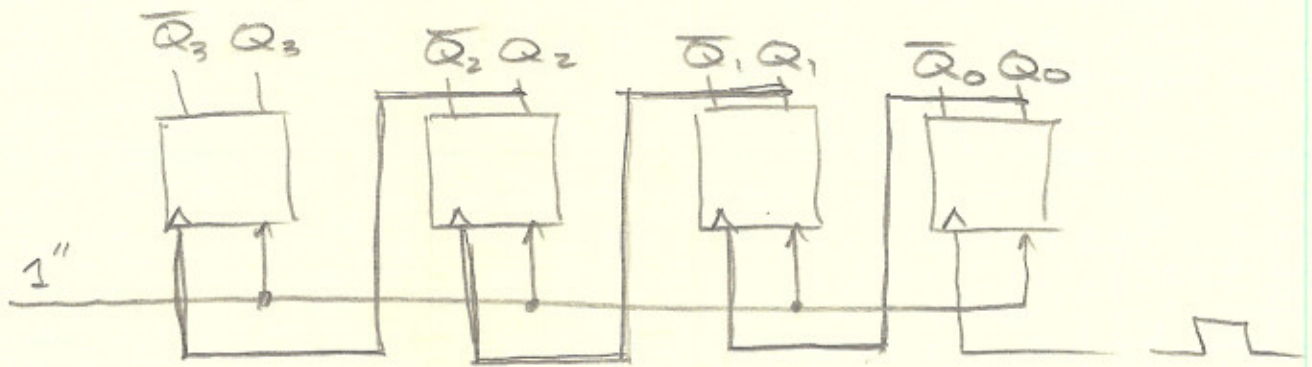
Down Counter



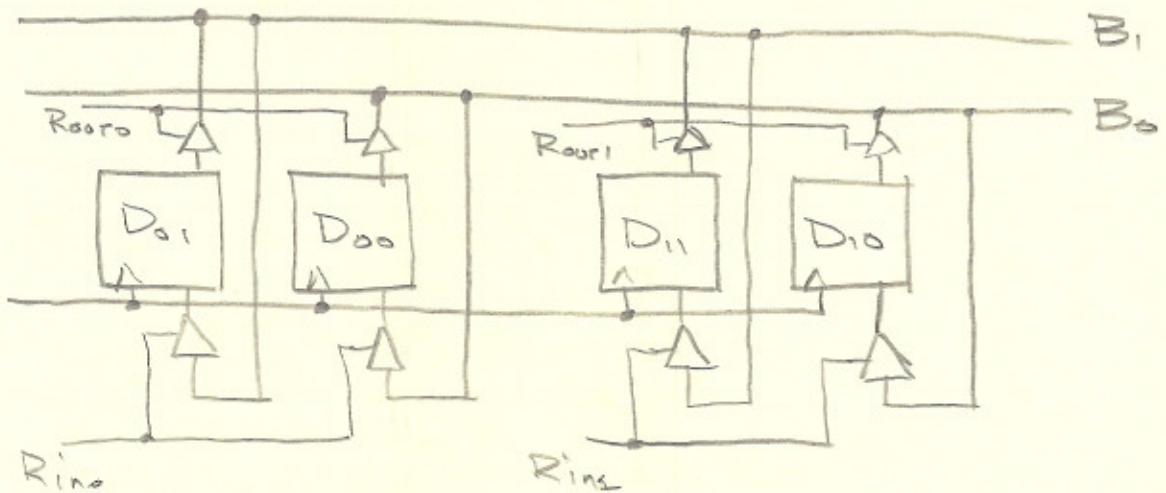
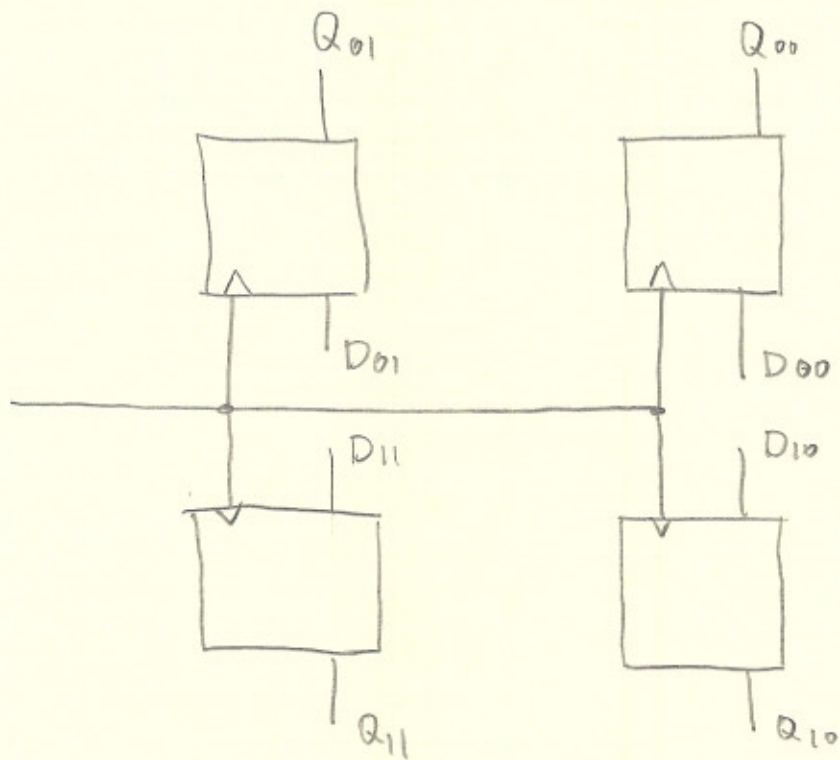
Reg Counter

(Values can be stored)

Can be "UP" or "down"



Simple Processor



The previous circuit can be drawn as.

